

8050 FLOPPY DISK

ALIGNMENT DISKETTE

Below are listed some items that have been corrected in the factory, but may still exist in the field:

1. Plastic standoffs that mount the drive should be 0.375 inches. Some machines have 0.500 inch standoffs. These can be detected by observing if there is interference between the disc bezel and the sheet metal of the cabinet. If there is interference, change to shorter standoff.

NOTE: Use only three (3) screws, with 3 flat washers, (2 in rear, 1 in front) to secure the drive to the base. Using 4 screws will throw drive out of alignment.

Drive Belts

Drive belts should have "shiny" or "black" side "in", ie: contact side.

Door Switch

Insure when closing door the motor spins screwing disc into hub. If switch is loose or adjusted wrong, disc will not be centered onto hub.

Test/Demo Disc

Make sure diskettes are inserted and door closed. If door is not closed while formatting, a "21" error will be reported.

Loading a Diskette

Do not "play" with door. Insert diskette and firmly close door. Do not tease it like 2040/3040.

Alignment

Before aligning a drive, check with a Test/Demo diskette.

Can you read on both drives? y/n

N - Check drive speed 200 Milliseconds/Rev. y/n

N - Adjust drive speed to 200 Milliseconds/Rev. Recheck if you can read Diskette.

Y - Copy demo diskette from 0 1. Can you read new diskette on both drives? y/n

N - Check drive speed 200 Milliseconds/Rev

Y - You have eliminated the drives as a problem.

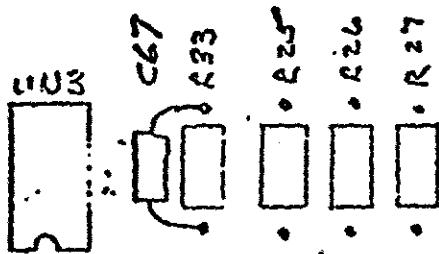
ECO CONTROL NUMBER

ENGINEERING
CHANGE ORDERECO NO. 1773
RELEASE DATE 2/22/93

SIZE	DOCUMENT NO.	CUR. REV	NEW REV	TITLE : P.C.B ASSY FLOPPY DIGITAL
2 1/2	44500102	F	G	
USED ON (MODEL)	8954	ECO TYPE	<input checked="" type="checkbox"/> REGULAR <input type="checkbox"/> RECORD CHANGE <input type="checkbox"/> TEMPORARY	REASON FOR CHANGE : FIX RACE CONDITION FROM GCR ECM (UKL) AND RIPPLE ENERGY (UHS)
CHANGE DISPOSITION		REMARKS OR INSTRUCTIONS		EFFECTIVITY
<input type="checkbox"/> USE AS IS <input checked="" type="checkbox"/> REWORK PARTS <input type="checkbox"/> SCRAP PARTS <input type="checkbox"/> RETURN TO INVENTORY				AS OF SIGN OFF DATE OF THIS E.C.O.

DESCRIPTION OF CHANGE :

ADD Zener Diode (P/N 900462-44) AS SHOWN BELOW.



REFERENCE
COPY

REWORK INSTRUCTIONS:

SOLDER LEADS OF C67 TO
LEADS OF R33 AS SHOWN ABOVE.

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ENGINEERING CHANGE ORDER

ECO
NO. 1773

DOCUMENT NO.	CUR. REV	NEW REV
SD-51612	F	G

PARTS LIST CHANGE

WAS

QTY REQD	ITEM	DS	PART NO.	DESCRIPTION	REF DES	BEND	NOTES
1	53		24-1442-114	24-1442-114 24-1442-202 114		25-6312	

QTY REQ'D	ITEM	DS	PART NO.	DESCRIPTION	REF DES	BEND	NOTES
2	5-3	744462-44	"	CHROME PLATE, ALUMINUM, 21/2 X 1/2"	744462	200, "F"	

MICROPOLIS DRIVE ALIGNMENT

Radial head alignment of the 8050 Micropolis drive unit is accomplished in the same general manner as for the 2040 Shugart drive - i.e: the stepper is positioned to the alignment track, and the head is then adjusted for correct lobe amplitudes of the "cat's eye" pattern.

The essential difference between the 8050 and the 2040 is the higher track and bit density of the 8050. This higher density makes adjustment quite difficult and, as such, re-alignment of the 8050 drive should not be attempted unless absolutely necessary and only if the technician is thoroughly familiar with head alignment procedures and pitfalls.

Following is the procedure for alignment of the 8050
Micropolis drive unit:

A. The following tools will be required:

- a). Commodore 8050 Alignment Disk.
- b). Formatted 8050 Disk Named "AC".
- c). #2 Phillips Screw Driver.
- d). 3/32 and 7/64 Hex Socket Wrenches.
- e). 1/4 inch Open End Wrench.
- f). Dual Channel Oscilloscope.

- B. Remove the drive from the system case, noting the location of the mounting screws. (If drive 0 is to be re-aligned, remove first the analog board and head shield board).
- C. Fabricate and connect extension cables for the drive signals (stepper, drive, door switch and head).
- D. Load the program "Microp Adjust" into the PET computer.
- E. Attach two oscilloscope probes, differentially added at 20 MV/CM, to test points TP1 and TP2 on the analog board. Sync the scope (AC, positive going edge) at pin 21 of UK3 on the digital board.
- F. Run the adjustment program, following carefully all procedures as listed on the computer screen.
- G. Following are notes on some of the test steps:
- 1). Step 1 - If this drive is to be checked only, this step should be skipped. This will allow verification of alignment without disturbing the setting of the collar.
 - 2). Step 2 - The door switch adjustment is very critical as this switch runs the drive motor while the disk is being seated. If the motor does not run sufficiently, the disk will be misaligned.

3). Step 4 - The belt test checks for spindle speed variation between two successive revolutions caused by belt slippage. This slippage has caused formatting errors. Check for the "Black/Shiny" side of belt "in". If it is not, correct same belt and retest. If it still fails, replace belt and retest.

4). Step 6 - In this step, if the lobe height differential is and remains within 20% after any hysteresis step, the drive has acceptable alignment. If the differential exceeds 20%, then the drive should be re-aligned.

Due to the variances between alignment disks, re-alignment of a drive must be done to the accuracy directed in this step. Failure to do so will result in the destruction of valuable data when writing on either misaligned drives or disks.

The stepper limit plate must be removed to gain access to the left stepper mount screw. This plate must be re-instated at a distance of $1/16 +/ - 1/32$ inch after alignment.

The object of alignment is to obtain balanced lobe height differentials after each hysteresis step

(i.e: not only must the differential be less than 15%, but the differential must "change sides" and have the same absolute value (within 5%) after each hysteresis step). For example, if the values after an hysteresis step are left lobe 7cm and right lobe 6.5cm (differential = 7.1%), then after the next successive hysteresis step the lobes must have a differential height of -12% to - 2% (right lobe higher).

This step must be reiterated sufficiently (including "bumping" to normalize the mechanics) to insure correct alignment.

Note that when the stepper screws are tightened, the alignment will shift approximately 5% (left lobe will be an additional 5% higher than right after the screws are tightened). The alignor must compensate for this by making the left lobe slightly lower before tightening the screws.

5). Step 8 - Hold the collar against the stop during tightening. This is critical to the correct location of Track 1.

6) Step 9 - The "AC" Disk is a formatted 8050 disk that will verify that the drive can read and write on a standard disk. This disk should be formatted on a known good alignment drive.

SUMMARY OF DRIVE 0

NUMBER OF PASSES: 3

TOTAL ERRORS= 0

COUNTABLE ERRORS= 0

FIRST PASS RETRIES= 0

SUMMARY OF DRIVE 1

NUMBER OF PASSES: 1

TOTAL ERRORS= 19

COUNTABLE ERRORS= 3

FIRST PASS RETRIES= 22

TRACK	ERRORS	1ST RETRIES
4	8	0
15	9	9
32	2	13

ERROR TYPE (2-16): 0 17 0 0 0 0 0 0 0 0 0 0 0 0 0 2

ERROR TIME: 19

RECOVERY COUNT:

16 0 0 0 1 0 0 0 0 2

OF BY RE.

CUT

SOFT ERROR TEST

I). INTRODUCTION

The soft error test exercises a disk system for a prolonged interval to check for errors. The program runs both 2040 (any DOS version) and 8050 systems from any computer.

In addition to the soft error tests, the program also tests disk quality, drive speed, belt quality and provides an error log reader.

All descriptions following are applicable to both 2040 and 8050 systems unless otherwise noted:

II). SOFT ERROR TESTS

A). GENERAL.

The soft error test is designed to verify the operational quality of a disk system. In its longest form - used for factory burn-in, the test formats a disk in each drive, lays down a worst case pattern and then reads the pattern, stepping track to track in both directions, for 500 passes on each drive. The program keeps a log on each disk of the number of passes and the number, location, type and time of errors.

The long test requires about 8 hours on 8050 and 6 hours on the 2040. Shorter versions, with and without formatting, are included to provide a faster verification of a system's operation.

The program runs completely free of the IEEE bus (to facilitate multiple systems testing) on device address 8.

B). ERROR STATUS INDICATION

During program run time, error and status indication is presented through the error and drive LED's as follows:

- 1). Error LED off and drive LED's off: Test not running.
- 2). Error LED off, one drive LED blinking evenly at high speed: The drive is formatting - this takes approximately five minutes/drive.
- 3). Error and drive LED(s) blinking evenly at high speed: The drive(s) either were unable to complete the formatting procedure, or were unable to write/read the log. This condition is due to either a bad (scratched) disk, inoperative system, or (formatting only) a bad belt.

4). Error LED off, one drive LED blinking randomly at medium to high speed: The drive is writing or reading the disk. If the random blinking slows occasionally, the drive is most likely making errors (which will be indicated later).

5). Error LED and drive LED(s) blinking slowly: The drive(s) has made an excessive number of errors on the first pass of the test. This is most likely due to a bad disk. (Refer to Para III D).

6). Error LED blinking slowly, drive LED steady on: The system is indicating errors made on that drive. Errors are counted out after each pass on each drive. The error blinking is truncated to a maximum of 16 to speed up the test. (All errors made are recorded on the log track).

7). Error LED off, both drive LEDs blinking slowly: The test has come to a normal end (with or without errors). To restore normal operation, turn primary power off and then on.

III). ERROR LOG READ AND INTERPRETATION.

Typing "0" or "1" will read the log on the respective drive. (If a printer is connected, the program will print out the log data automatically). In the event an error is made in reading the log, the program will do two additional tries before giving up. The error message on the PET screen shows the type of error. (Ref. Para III F).

Following is the interpretation of the log:

A). Number of passes: Total passes on the drive for this test.

B). Total errors: Total errors made during test. The "total errors" count is incremented only once for each error, regardless of how many retries are required for recovery.

C). Countable errors: The number of errors that required more than one retry to recover.

D). First pass retries: Determined for the first pass only, this count shows the total retries made minus the

first retries that were successful. (Obtained by multiplying the recovery counts by their respective position, disregarding the first position). This value is a measure of disk quality in that it shows how many stumbles (beyond the first) were made during the first pass of the test. If this count exceeds 5, the disk is declared bad and the test is aborted.

E). Track, error, retries: This matrix print will show a bad disk if a large majority of errors are on one track (or two adjacent tracks).

F). Error type: This shows how many of each type of error has occurred. The error types are:

- #2 - Block header not found
- #3 - No sync
- #4 - Data block not present
- #5 - Checksum error in data block
- #6 - Not used
- #7 - Verify error
- #8 - Write with write protect on
- #9 - Checksum error in header block
- #10 - Data overlay into next header
- #11 - Disk ID mismatch

#12 - 15 not used

#16 - Decode error

G). Error time: This shows how many errors per 100 passes, modulo 100.

H). Recovery count: How many retries it took to recover errors (a maximum of ten recovery attempts are made for each error).

At the fifth recovery attempt on an 8050, the head is hysteresis stepped 1/4 track in an attempt to recover a misaligned data block.

IV). DRIVE TESTS

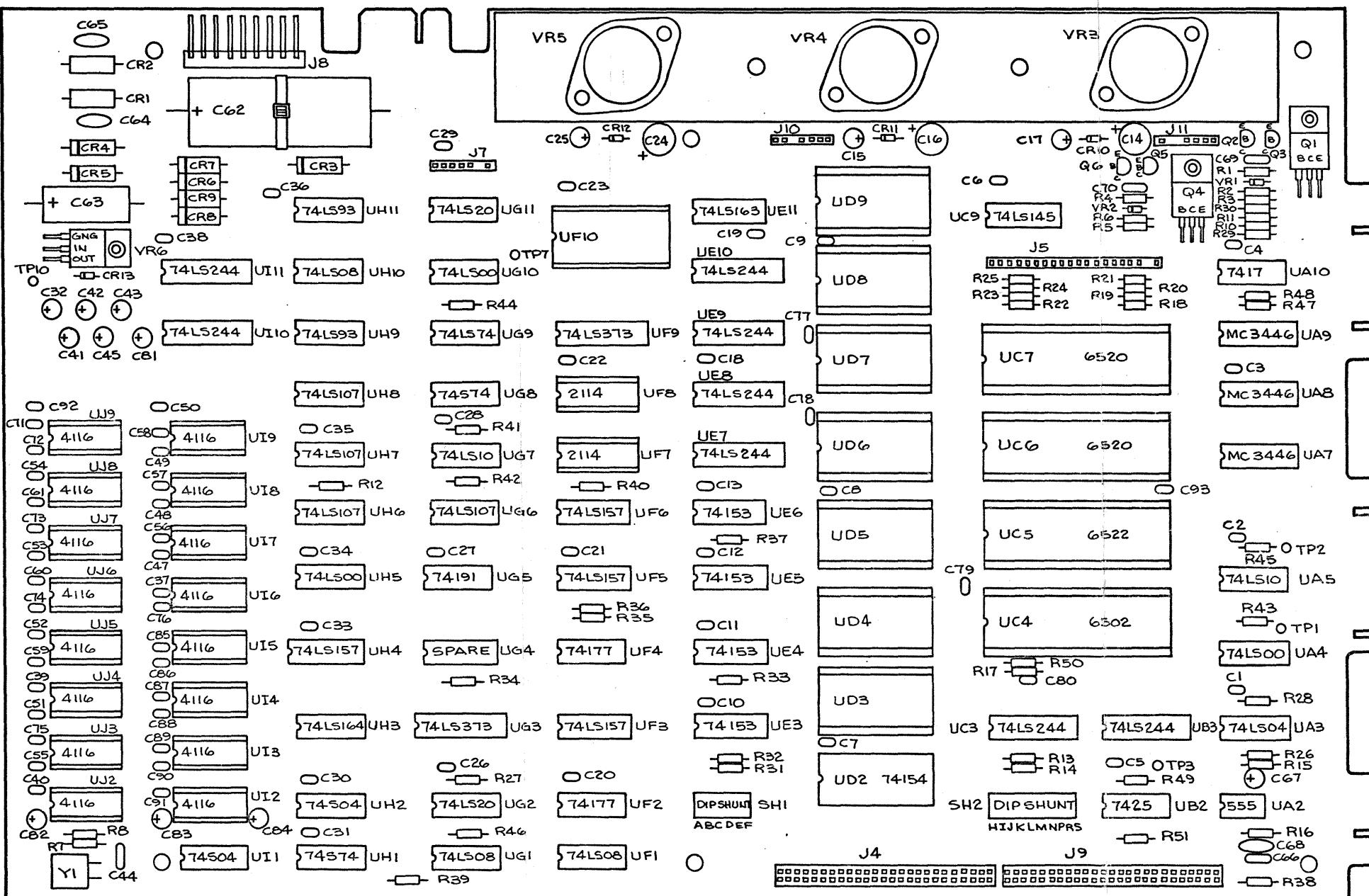
Prior to starting a test, the drive speed and belt should be checked by typing "D" followed by "0" or "1". These functions are implemented for the 8050 system only at this time.

The belt is checked by measuring the period difference between two successive revolutions. This value is measured twenty times. If any one value is greater than 0.5 MS or if two or more are greater than 0.4 MS, the belt is considered bad and should be replaced. (A

typical belt will have a maximum value of 0.3 MS).

This test is necessary as the format program requires a constant speed for correct positioning of the sectors.

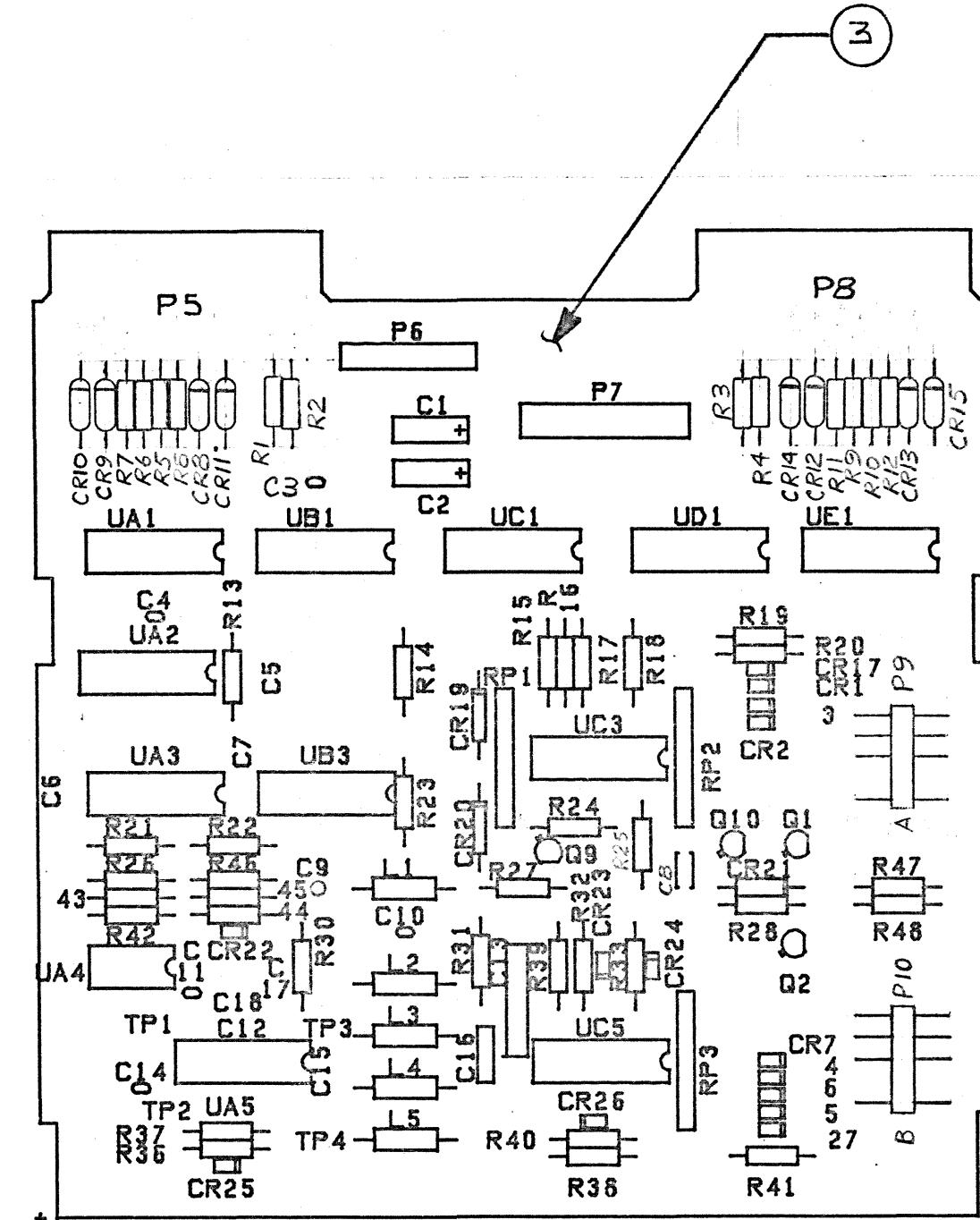
REVISIONS				
LTR	ZONE	DESCRIPTION	DATE	APPROVED



<u>UNLESS OTHERWISE SPECIFIED</u>			DRAWN BY:	DATE	
TOLERANCES ON: DECIMALS			CHKD:		
X	XX	XXX	ENGR:		
±	±	±	APPR:		
MATERIAL:			USED ON	NEXT ASSY	
FINISH:					
SIZE C				REV	
SCALE	SHEET	OF			

REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1		



DRAWING NO.	TITLE	CURRENT REV
320815	FABRICATION DWG	H
320815	ARTWORK	H
320816	SCHEMATIC	D

-01 SHOWN

UNLESS OTHERWISE SPECIFIED				DRAWN BY:	DATE
TOLERANCES ON: DECIMALS				J. SCOTT	9-11-79
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MATERIAL:				APPR:	
FINISH:				USED ON	NEXT ASSY
				320803	
SIZE B		320817		REV L	
SCALE 1/1		SHEET 4 OF 4			

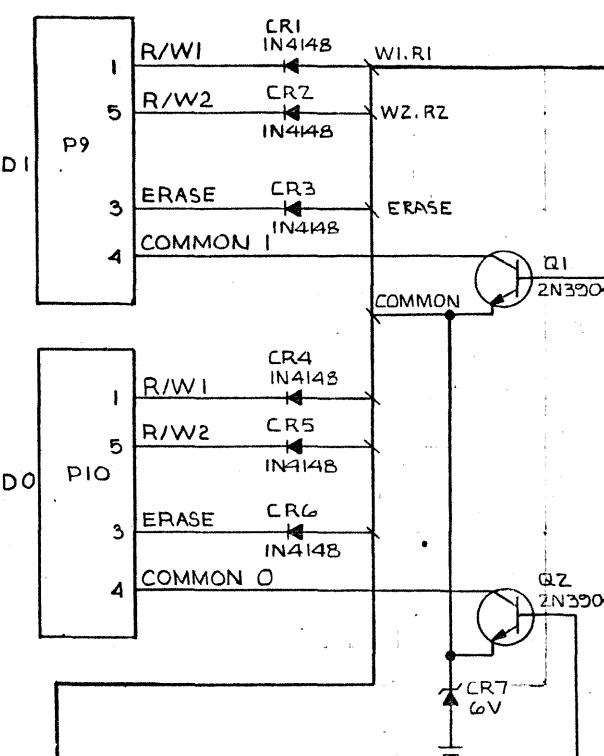
commodore

PC BOARD ASSY
FLOPPY ANALOG

REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED
4		PRE-RELEASE		
A		RELEASED	2/15/79	J.T.
B		REVISED PER ECO 1150	1-3-79	P.C. 100
C		REV PER ECO 1310	10-2-79	P.C. 100
D		REVISED PER ECO 1424	11-9-79	J.P.

HEAD 1



WI CR17

IN4148 Q2T2905

R20 20K

C3 Q2T2905

R24 1% 604

RP2 330

R25 1% 2.26K

RP2 330

R18 1% 2.26K

B1 7416

R23 2K

P9

B3 74LS74

Q PR

R2 2.26K

C2 4.7 25V

+12VA

C1 4.7uf 25V

+12VA

C3 2.26K

+12VB

+5V

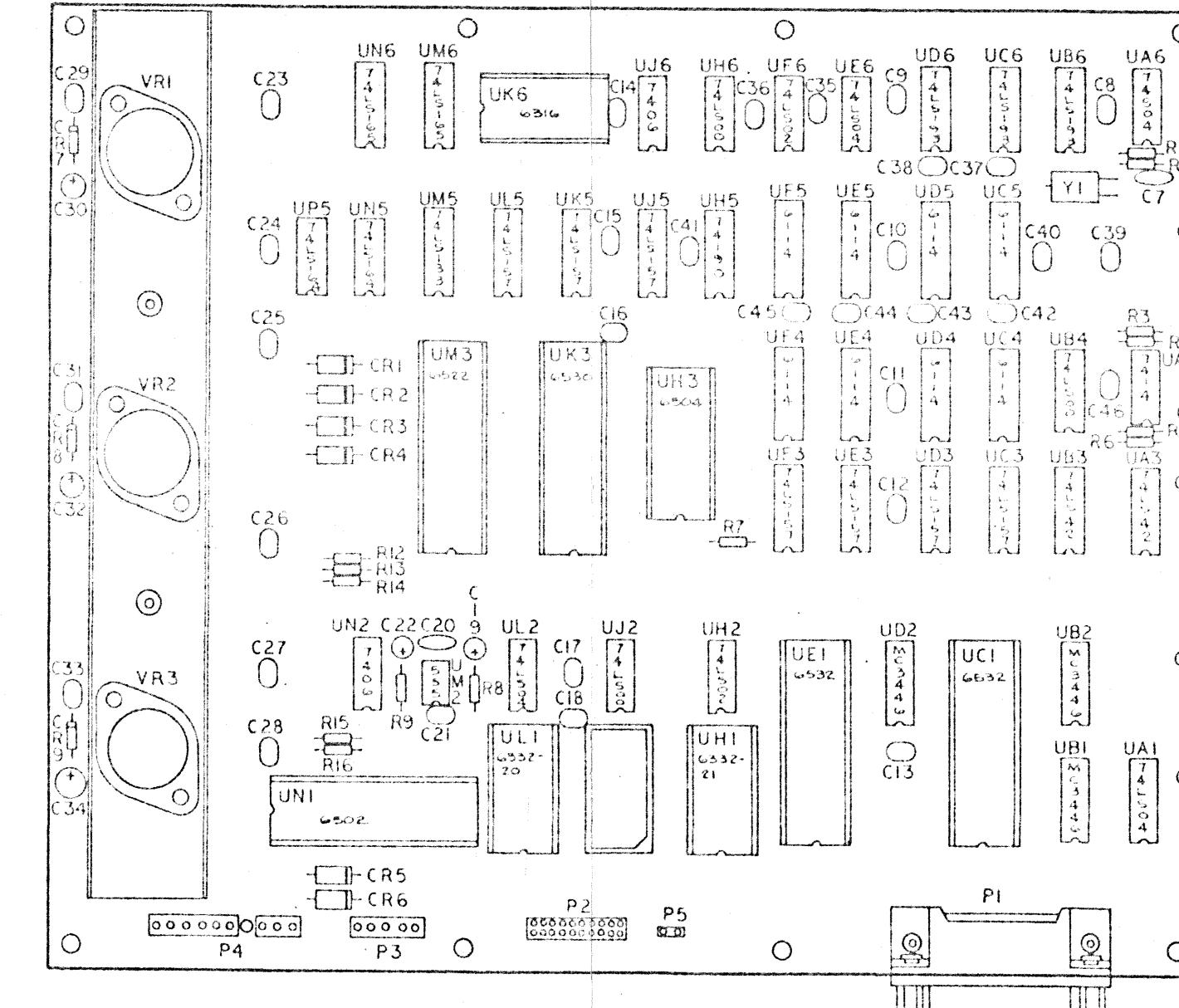
+12VB

+5V

+12VA

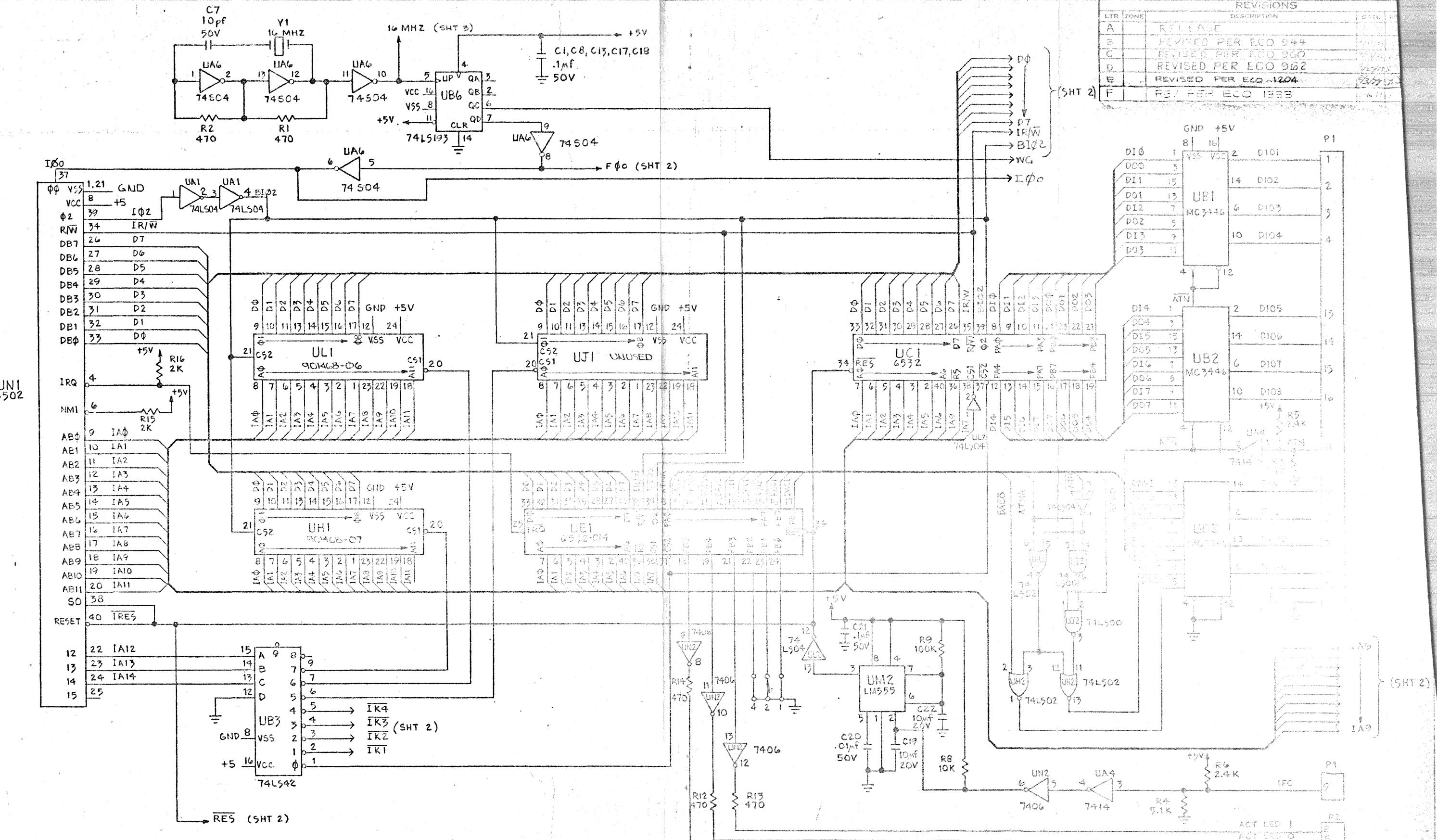
REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED



UNLESS OTHERWISE SPECIFIED TOLERANCES ON DECIMALS			DRAWN BY		DATE	
X	XX	XXX	CHRD	ENGR	APPR	
X	S	X				
MATERIAL			USED ON		NEXT ASSY	
FINISH						
SIZE C						
SCALE			SHEET OF			

commodore



NOTES:

- I. RESISTORS ARE $\frac{1}{4}$ W, 5% UNLESS OTHERWISE SPECIFIED.
VALUES ARE EXPRESSED IN OHMS.

REVISIONS			
NE	DESCRIPTION	DATE	AP
	RELEASE	10-10-74	
	REVISED PER ECO 944	10-10-74	
	REVISED PER ECO 960	10-10-74	
	REVISED PER ECO 962	10-10-74	
	REVISED PER ECO 1204	03-17-75	
	REV PER ECO 1333	10-11-75	

		GND	+5V		P1
I/O	t	8	16		
DD		VSS	VCC	2	D101
I1	5				1
I2	15			14	D102
I3	13	UB1			2
I2	7	MC3446		6	D103
I2	5				3
I3	9			10	D104
I3	11				4

13	14
15	16
17	18
19	20
21	22

The diagram illustrates the assembly process of a printed circuit board (PCB). It shows a central rectangular PCB with various electronic components mounted on it. A vertical metal stand-off is positioned in the center-left of the board. A horizontal metal bracket is attached to the top edge of the board. A circular component, likely a connector or inductor, is mounted near the bottom center. A small cylindrical component is located at the bottom left corner. The assembly process is depicted with dashed lines and arrows indicating the sequence of steps: 1. Mounting the central component, 2. Attaching the metal stand-off, 3. Mounting the metal bracket, 4. Mounting the circular component, and 5. Mounting the small cylindrical component.

1960-1961

+5V

RG
2.4K

IFC

84
5.1K ACT LED 1
5.1K ACT LED 2

Symptomatology

LOGIC DIAGRAM DIGITAL SCM

DIGITAL FLOPPY FOR DSKY

1350000000

1920-1930

10. The following table shows the number of hours worked by 1000 employees in a company. Calculate the mean, median, mode and range.

REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED
SEE SHEET 1				

(SHT 3) {
 EΦ Y
 E1 Y
 I2 Y
 E2 Y
 E3 Y
 I7 Y
 E6 Y
 }

(SHT 3) {
 SYNC
 WPS
 (SHT 1)
 RES
 }

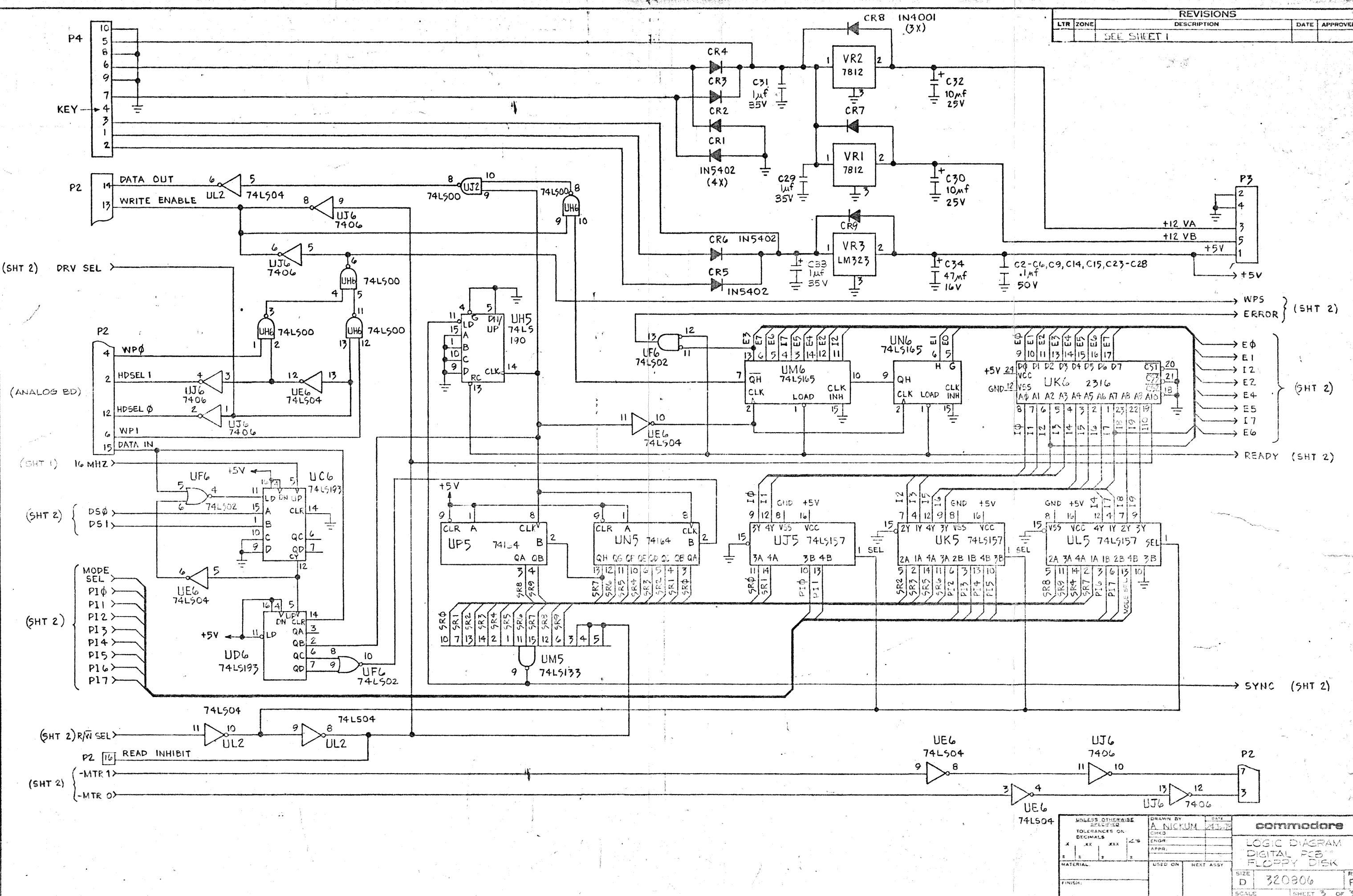
28 F02
 R/W
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 FA12
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 IK2
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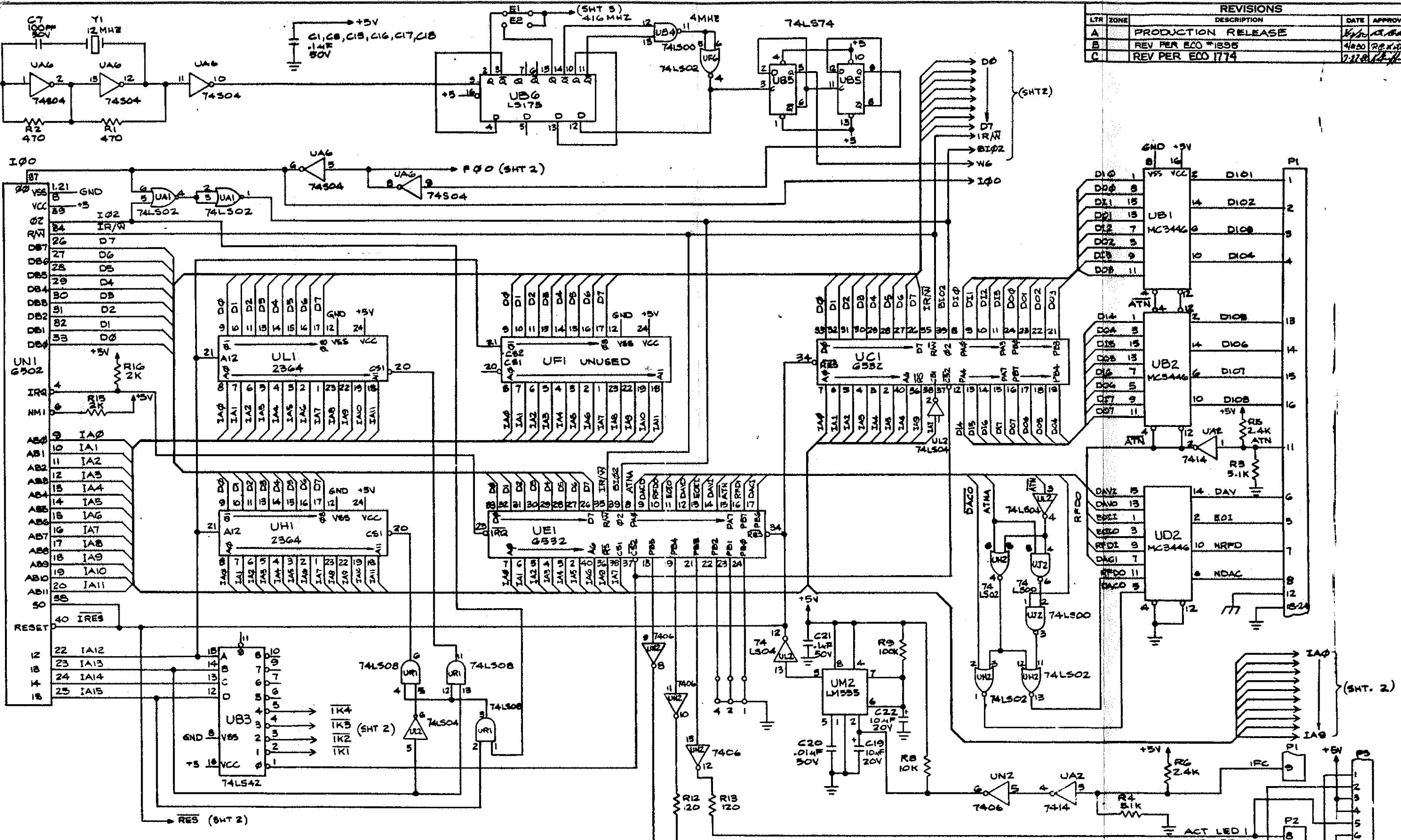
26 FR/W
 25 DΦ
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 21 D4
 20 D5
 19 D6
 18 D7
 3 IRQ
 1 RES
 2 GND
 4 +5V
 5 FAΦ
 6 FA1
 7 FA2
 8 FA3
 9 FA4
 10 FA5
 11 FA6
 12 FA7
 13 FA8
 14 FA9
 15 FA10
 16 FA11
 17 FA12
 BIΦ2
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 IK1
 IR/W
 IA9
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 IAΦ
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 VCC

27 DΦ
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 38 RS2
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 42 VSS
 43 VCC
 44 UM3
 45 6522
 46 GND
 47 +5V

5 UAI
 74LS04

DΦ
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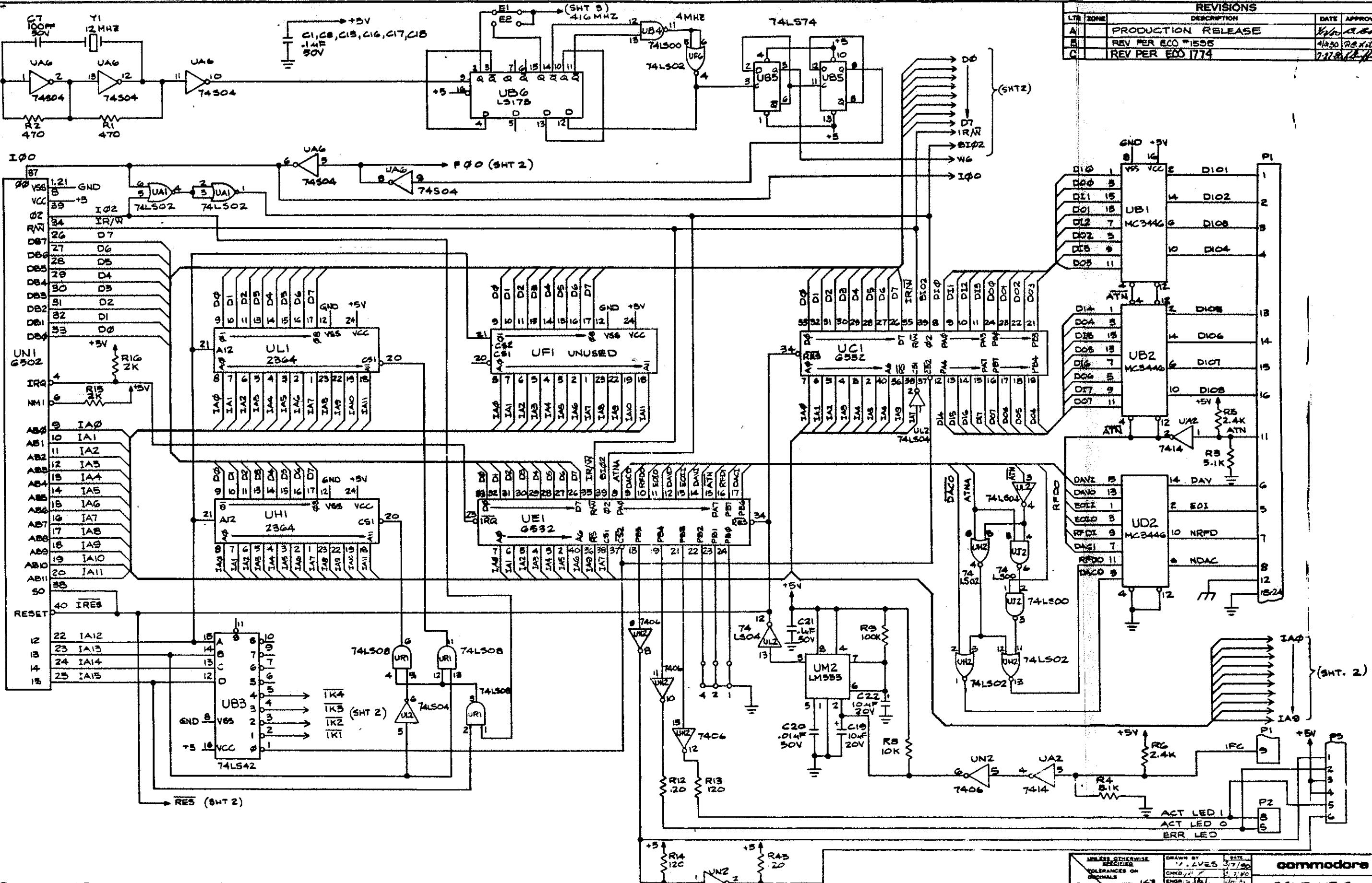
1. RESISTOR ARE IN OHMS, 1/4W, 5%.
NOTES; UNLESS OTHERWISE SPECIFIED:

REVISIONS			
LTR	ZONE	DESCRIPTION	DATE APPROVED
A		PRODUCTION RELEASE	4/10/01 R.C.BAL
B		REV PER EAO 1895	4/20/01 R.C.BAL
C		REV PER ECO 1774	7-11-01 R.C.BAL

MATERIALS, EQUIPMENT, SPECIFIED TOLERANCES ON DIMENSIONS		DRAWN BY V. LIVES	DATE 7/7/80
X	22	EMERG. 23A	5/1/80
S	21	APPR.	7/7/80
B	20	REV.	1
MATERIAL:		USED ON	NEXT ASSET
		FLOPPY	8050002
		8050	
PRINTED:			

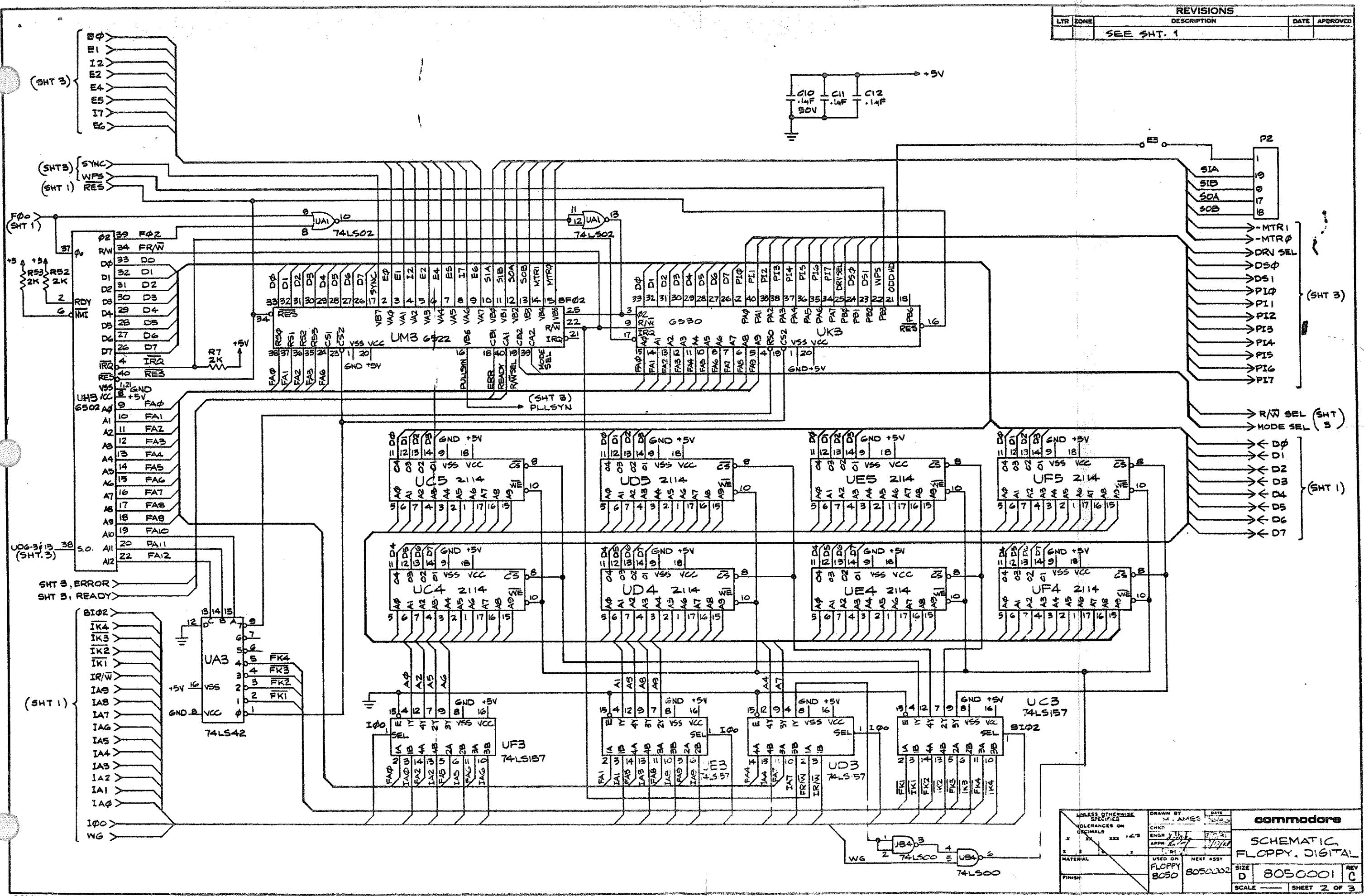
commodore

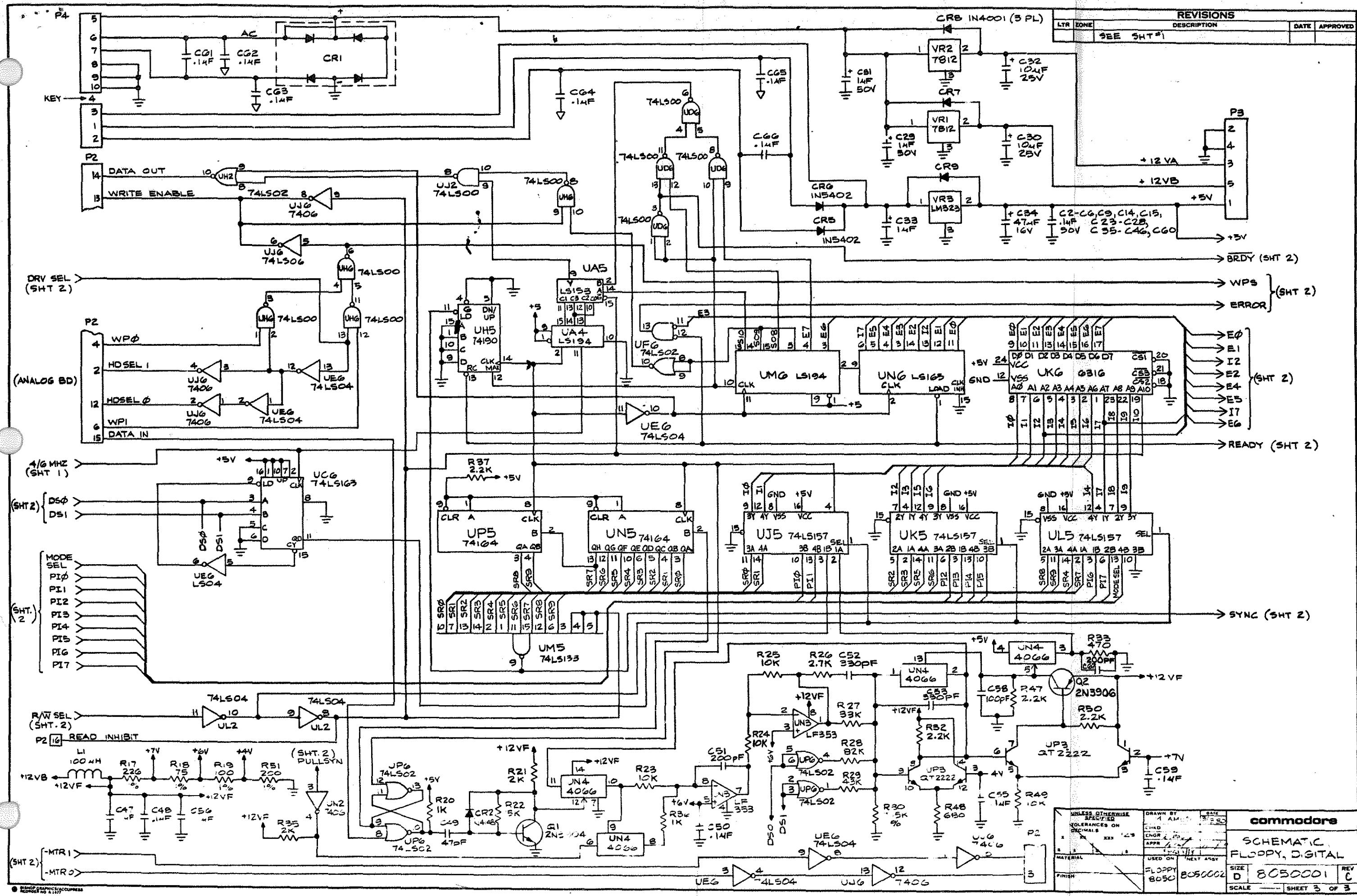
**SCHEMATIC
FLOPPY, DIGITAL**



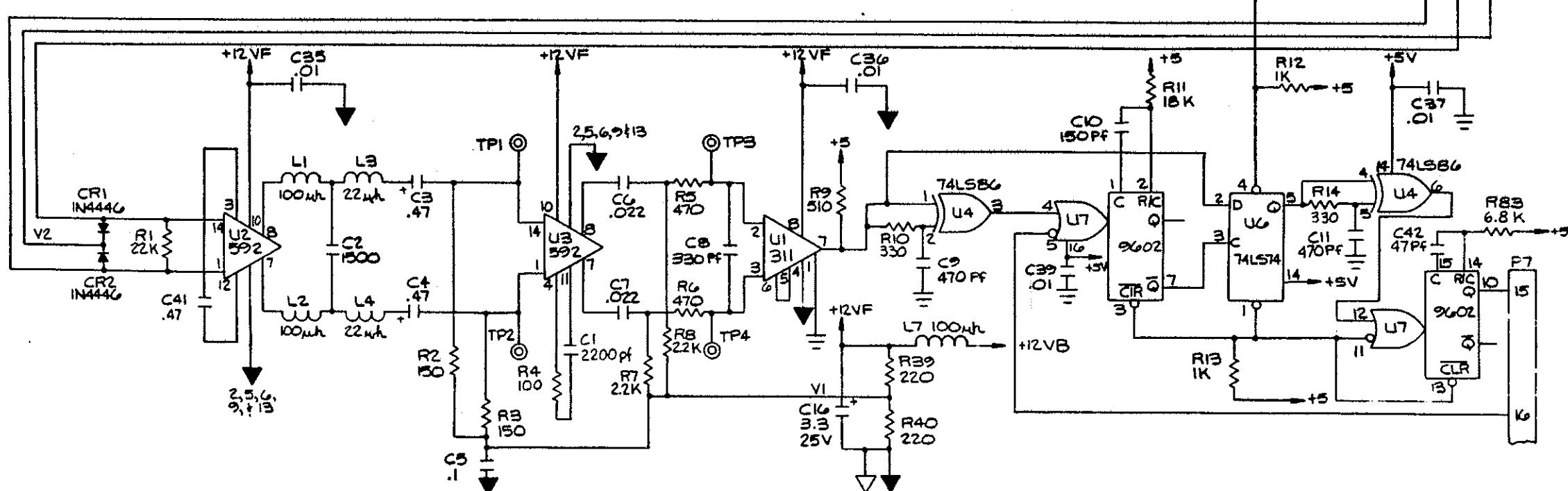
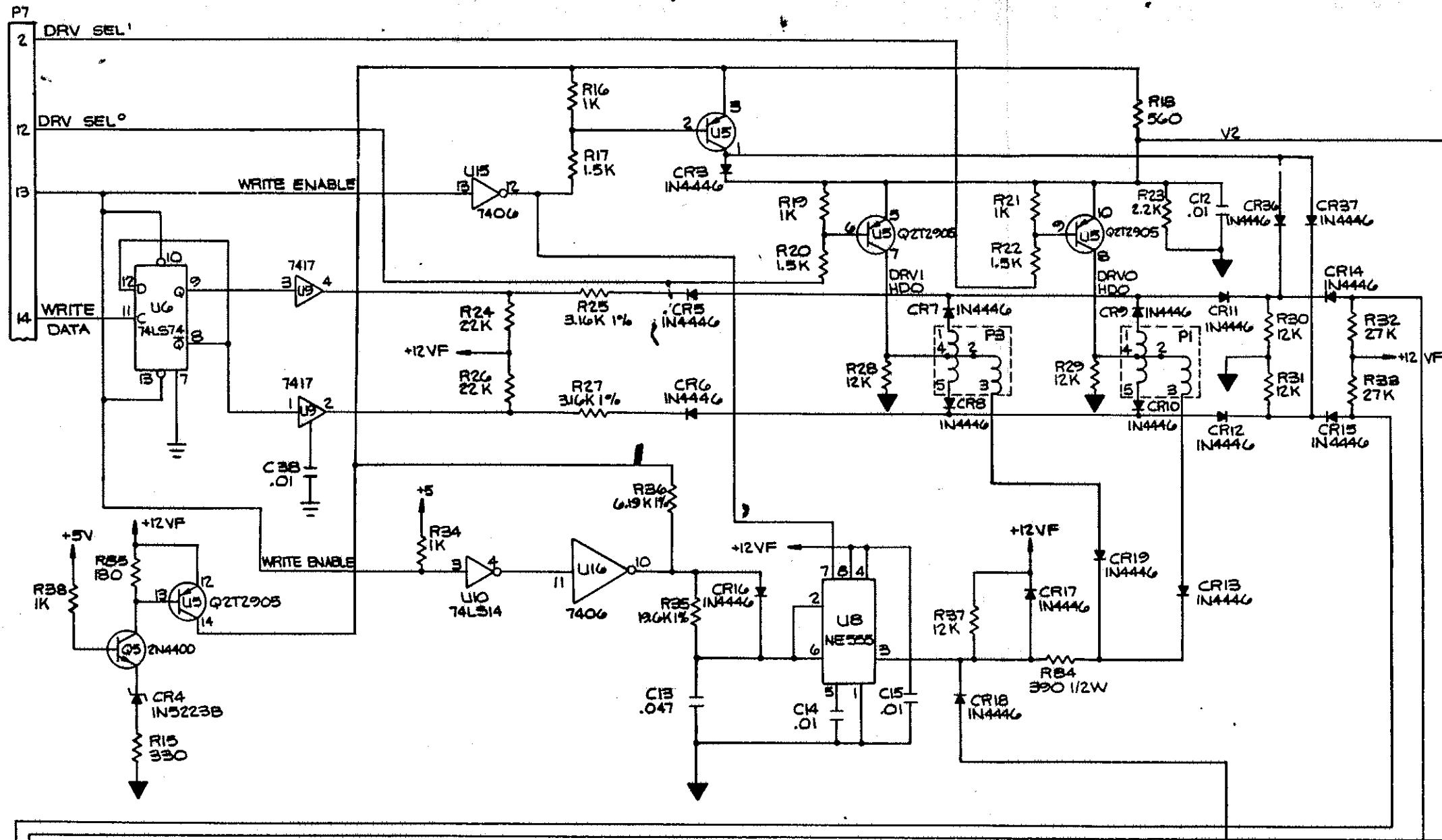
1. RESISTOR ARE IN OHMS, 1/4W, 5%.
NOTES; UNLESS OTHERWISE SPECIFIED:

MATERIAL OTHERWISE SPECIFIED TOLERANCES ON DIMENSIONS		DRAWN BY V. LIVES	DATE 3/7/90	commodore	
		CHNO	1	3.7.90	
		ENGRA	2/16	10/1	
		APPR	7	1/1	
MATERIAL:		USED ON	NEXT ADRY		
		FLOPPY			
FINISH:		8050	8050002		
SIZE		D 8050001			REV C
SCALE		SHEET 1 OF 3			





REVISIONS				
LTR	EDITION	DESCRIPTION	DATE	APPROVED
A		PRODUCTION RELEASE	3/10/01	B. Miller
B		REV PER ECO 1833	3/20/01	B. Miller

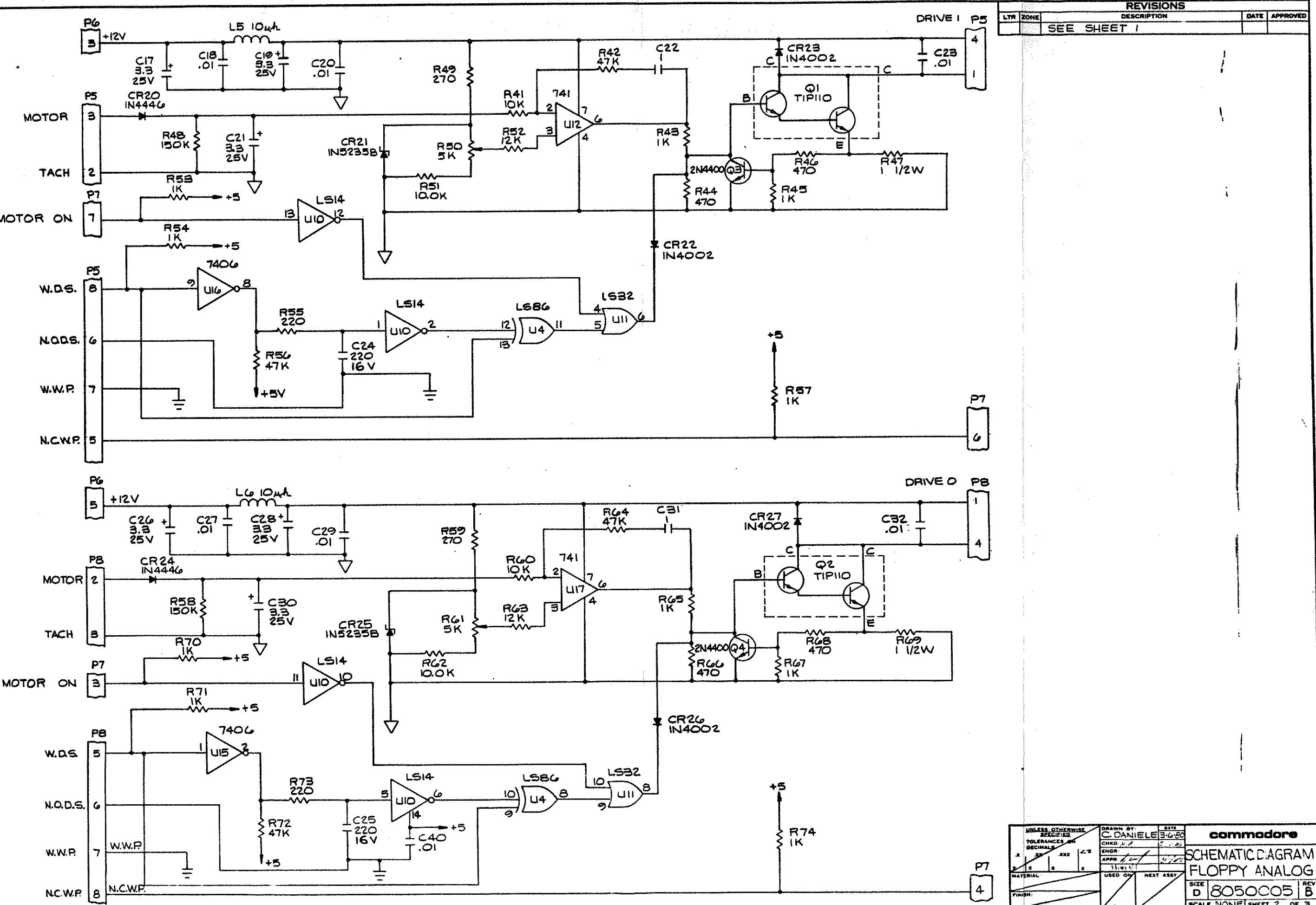


2. ALL CAPACITOR VALUES ARE IN MICROFARADS
AND ARE 50 VDC.

4. ALL RESISTOR VALUES ARE IN OHMS $\pm 5\%$.
ARE 1/4 WATT.

NOTES - UNLESS OTHERWISE SPECIFIED:

UNLESS OTHERWISE SPECIFIED		DRAWN BY W. CAPELLE	DATE 3-1-80	commodore	
TOLERANCES ON DIMENSIONS		CHKD HCR	APPR HCR		
X	3.00	XXX	-4.0		
+	-.1				
MATERIAL		USED ON	NEXT ASSY		
PARTS:					



© BOMI GRAPHICS/ACCPRESS
RENDERING NO A-107

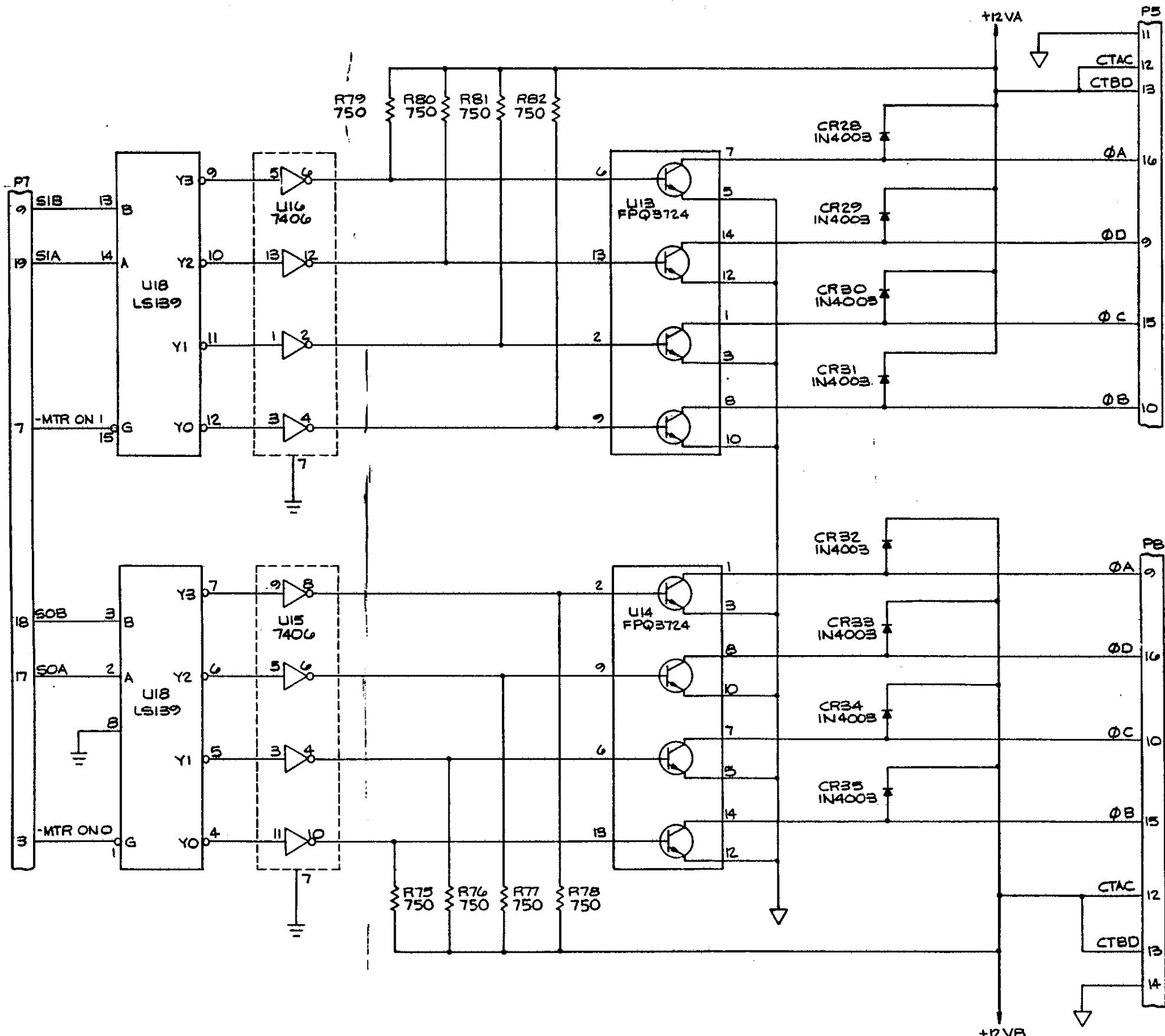
UNLESS OTHERWISE SPECIFIED		DRAWN BY	C. DANIELLE	DATE	3-6-80
TOLERANCES PER DECIMAL		CHkd	4/1	ENGR	4/1
MATERIAL		APPR	100%	REV	B
FINISH		USED ON	NEXT ASSY		
SIZE D 8050005		REV			
SCALE NONE		SHEET 2 OF 3			

commodore

SCHEMATIC DIAGRAM
FLOPPY ANALOG

REVISIONS

REV.	DATE	APPROVED
		SEE SHEET 1



1. NO. OF OTHER DRAWINGS	2. DRAWN BY	3. DATE
2	C. CANIELE	5/6/87
4. TOLERANCES ON	5. CHECKED	6. APPROVED
DEGREES	5/6/87	5/6/87
7. ENGR	8. APPR	9. DATE
		5/6/87
10. MATERIAL	11. USED ON	12. NEXT ASSTY
PCBOARD		
13. P/N	14. REV.	15. SIZE
8050005	B	REV. B
16. SCALE	17. SHEET	18. OF
NONE	3	5

commodore

SCHEMATIC DIAGRAM
FLOPPY ANALOG

PART NO.	DESCRIPTION
8050002-01	P.C.B. ASSY, FLOPPY, DIGITAL

REVISIONS		DATE	APPROVED
LTR	ZONE	DESCRIPTION	
A		PRODUCTION RELEASE	3/20/80 <i>ml</i>
B		REV PER ECO 1596	4/9/80 <i>ml</i>
C		REV PER ECO 1600	4/9/80 <i>ml</i>
D		REV PER ECO 1688	6/13/80 <i>ml</i>
E		REV PER ECO 1747	7/19/80 <i>ml</i>
F		REV PER ECO 1756	7-21-80 <i>ml</i>
G		REV PER ECO 1773	7-21-80 <i>ml</i>

2. IDENTIFY WITH COMMODORE
PART NO. 8050002 &
APPLICABLE DASH NO. LOCATE
APPROX AS SHOWN.

1. SHEET 5 OF 5 SIZE D
ASSY DWG
NOTES-UNLESS OTHERWISE SPECIFIED:

commodore	TITLE: P.C.B. ASSY, FLOPPY DIGITAL	DRAWN BY: <i>J. L. Johnson</i>	DATE 10/6/80	ENGR. <i>ML</i>	SIZE B	DRAWING NUMBER 8050002
© BISHOP GRAPHICS/ACCUPRESS REORDER NO. A7849		CHKD: <i>ML</i>	3/20/80	APPR: <i>ml</i>	SP/100	SHEET 1 OF 5

QUANTITY REQD PER PART / DASH NO.	ITEM	DS	PART NUMBER	DESCRIPTION	REF DES	BNL	NOTES
1	1	•	8050001	SCHEMATIC - FLOPPY, DIGITAL			
	2						
	3		8050003	PC FABRICATION - FLOPPY, DIGITAL			
	4						
	5		901435-01	LSI 6502 MPU	UN1, UH3		
	6		901437-01	LSI 6522 VIA	UM3		
	7		901483-03	LSI 6530 PIA	UK3		
	8		901458-01	LSI 6532 PIA	UC1, UE1		
	9		901467-01	LSI 6316 ROM	UK6		
	10		901482-03	LSI 2364 ROM	UL1		
	11		901482-04	LSI 2364 ROM	UH1		
	12		901453-03	LSI 2114 RAM	UC4, UC5, UD4, UD5, UE4, UES, UF4, UF5		
	13						
	14		901523-01	IC LM555 TIMER	UM2		
	15		901523-10	IC LF 353	UN3		
	16		901524-01	IC MC3446 BUS INTERFACE	UB1, UB2, UD2		
	17		901502-01	IC MC14166 CMOS, SWITCH	UN4		
	18		901521-03	IC 74LS08	UR1		
	19		901521-01	IC 74LS00 QUAD NAND	UB4, UD6, UH6, UJ2		
	20		901521-21	IC 74LS02 QUAD NOR	UAI, UF6, UH2, UP6		
	21		901525-01	IC 74S04 HEX INVERTER	UA6		
	22		901521-02	IC 74LS04 HEX INVERTER	UE6, UL2		
	23		901522-06	IC 7406 HEX INVERTER BUFFER	UJ6, UN2		
	24		901522-19	IC 7414 QUAD SCHMITT TRIGGER	UA2		
	25		901521-17	IC 74LS42 1-OF-10 DECODER	UA3, UB3		
	26		901521-06	IC 74LS74 DUAL D FLIP FLOP	UB5		
	27		901521-15	IC 74LS133 13-INPUT NAND	UM5		
	28		901521-25	IC 74LS153 DUAL 4-INPUT MUX	UA5		
	29		901521-11	IC 74LS157 QUAD 2-INPUT MUX	UC3, UD3, UE3, UF3, UJ5, UK5, UL5		
	30		901522-27	IC 74164 SI PO REG	UNS, UPS		
	31		901521-12	IC 74LS165 PI SO REG	UN6		
	32		901522-16	IC 74190 UP/DOWN DEC COUNTER	UH5		
	33		901521-33	IC 74LS163 UP/DOWN COUNTER	UC6		ITEM 36 MAY BE USED AS A SUBSTITUTE
	34		901521-34	IC 74LS175 QUAD D FLIP FLOP	UB6		
	35		901521-35	IC 74LS194 4-BIT UNIV. COUNTER	UA4, UM6		
S	36		901521-36	IC 74LS161 COUNTER	UC6		
	37						

commodore

TITLE: PCB. ASSY, FLOPPY, DIGITAL

DRAWN BY: J. P. Johnson	DATE: 1/16/81	ENGR: J. P. Johnson	DATE: 1/16/81	SIZE: C	REF: G	SHL: 2/5
CHKD: APPR: 1/16/81	3/15/81					

PART / DASH NO.	ITEM	DS	PART NUMBER	DESCRIPTION	REF DES	LEN	NOTES
-01	1 38	*	902550-01	TRANSISTOR QUAD NPN, Q2T2222	UP3		
	1 39		902658-01	TRANSISTOR ZN3904	Q1		
	1 40		902707-01	TRANSISTOR ZN3906	Q2		
	41						
	2 42		901528-04	VOLTAGE REGULATOR 7812 +12V/1.5A	VR1, VR2		
	1 43		901528-01	VOLTAGE REGULATOR LM323 +5V/3A	VR3		
	44						
	1 45		900556-03	CRYSTAL 12 MHz	Y1		
	46						
	1 47		900755-02	RECTIFIER, FULL WAVE BRIDGE	CRI		ITEM 48 MAY BE USED AS A SUB.
S 48			900755-03	RECTIFIER, FULL WAVE BRIDGE			CRI
	2 49		900753-01	DIODE IN5402 3A / 200V	CR5, CR6		
	3 50		900750-01	DIODE IN4001 1A / 50 V	CR7, CR8, CR9		
	1 51		900850-01	DIODE IN4148	CR2		
	49 52		900461-28	CAPACITOR, CERAMIC AXIAL .1uF	C1-6, C8-15, C21, C23-28, C35-48, C50, C55, C56, C59-66		
	2 53		900462-44		200 pF C51, C67		
	1 54		900461-16		.01uF C20		
	1 55		900462-29		47 pF C49		
	2 56		900462-49		330 pF C52, C53		
	2 57		900463-04	CAPACITOR, CERAMIC AXIAL .100 uF	C7, C8		
	3 58 B		900110-01	CAPACITOR, LOW LEAK, ELEC 1uF 50V	C29, C31, C33		SEE ITEM 74 FOR SUBSTITUTE
	1 59		900101-37	CAPACITOR, ELECTROLYTIC 47uF 16V	C34		
	2 60		900109-03	CAPACITOR, LOW LEAK. 10uF 20V	C19, C22		
	2 61		900101-04	CAPACITOR, ELECTROLYTIC 10uF 25V	C30, C32		
	1 63		901301-01	CHOKE RF SHIELDED 100 uH	L1		
	64						
	3 65		901550-58	RESISTOR 1/4 W ±5%	470 Ω R1, R2, R33		
	1 66		901751-17	RESISTOR 1/4 W ±1%	7.15K Ω R30		
	1 67		901751-09		75 Ω R18		
	1 68		901751-18		100 Ω R19		
	1 69		901751-19		226 Ω R17		
	1 70		901751-20	RESISTOR 1/4 W ±1%	200 Ω R51		
	2 71		901550-01	RESISTOR 1/4 W ±5%	1K Ω R20, 36		
	2 72		901550-03	RESISTOR 1/4 W ±5%	5.1K Ω R3, R4		
	1 73		901550-07	RESISTOR 1/4 W ±5%	100K Ω R9		
	5 74 B		900101-35	CAP, ELECTROLYTIC 1uF 25V			SUBSTITUTE FOR ITEM 58. C29, C31, C33

commodore

TITLE: P.C.B. ASSY, FLOPPY, DIGITAL

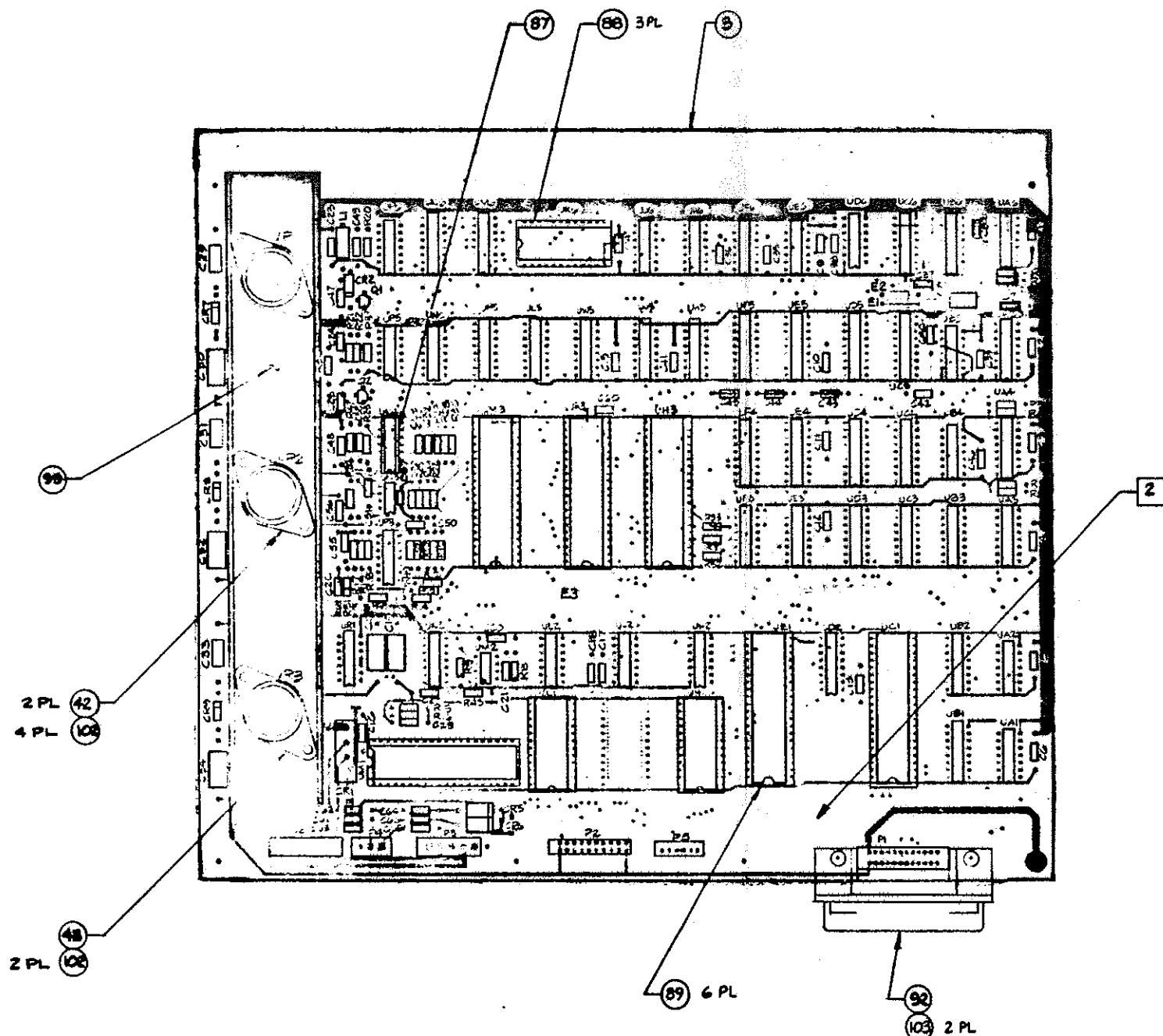
DRWN BY: 2/10/84	DATE: 13/10/84	ENGR: P. J. H.	DATE: 13/10/84	SIZE: B	REV: G	SHT: 3/5
CHKD: M.L.	3/24/84	APPR: P. J. H.	3/24/84			

QUANTITY	ITEM	PART / DASH NO.	ITEM	DS	PART NUMBER	DESCRIPTION	REF DES	BEND	NOTES					
									01	02				
	1	75	B		901550-99	RESISTOR 1/4 W ±5%	43K Ω	R29						
	4	76	B		901550-18		2.2K Ω	R52,R32,R17,R57						
	5	77	B		901550-20		10K Ω	R8,R23,R24,R25,R49						
	7	78	B		901550-53		2K Ω	R15,R16,R7,R21,R35,R53,R52,						
	4	79	B		901550-75		120 Ω	R45,R12,R13,R14,						
	2	80	B		901550-85		2.4K Ω	RS,R6						
	1	81	B		901550-26		15K Ω	R22						
		82												
	1	83	B		901550-06		33K Ω	R27						
	1	84	B		901550-23		2.7K Ω	R26						
	1	85	B		901550-95	RESISTOR 1/4 W ±5%	82K Ω	R28						
	1	86	B		901550-31	RESISTOR 1/4 W ±5%	680 Ω	R43						
	1	87	B		904150-01	IC SOCKET 14 PIN	XUIN4							
	3	88	B		904150-04	IC SOCKET 24 PIN	XUHI,XUK6,XULI	ITEM 90 MAY BE USED AS A SUB.						
	6	89	B		904150-06	IC SOCKET 40 PIN	XUC1,XUE1,XUH3,XUK3,XUM3,XUN1	ITEM 91 MAY BE USED AS A SUB.						
	5	90	B		904153-03	IC SOCKET 24 PIN								
	5	91	B		904153-05	IC SOCKET 40 PIN								
	1	92	B		903206-01	CONNECTOR RT ANGLE IEEE-488	P1							
	1	93	B		903344-10	HEADER DUAL IN LINE, 20 PIN .100 SPACE	P2							
	1	94	B		903316-05	HEADER .156 CENTER 5 PIN	P3							
	1	95	B		903316-03	HEADER .156 CENTER 3 PIN	P4							
	1	96	B		903316-06	HEADER .156 CENTER 6 PIN	P4							
	1	97	B		903326-06	HEADER .100 CENTER 6 PIN	P5							
		98												
	1	99	C		320268-01	HEAT SINK								
A	100	B			904907-01	HEAT SINK COMPOUND THERMAL								
		101												
	6	102	B		906403-19	RIVET DOME HEAD OPEN END								
	2	103	B		906403-03	RIVET DOME HEAD OPEN END								
		104												
	1	105	B		320176-05	WIRE JUMPER 22 GA	E1							
commodore		TITLE:	P.C.B. ASSY, FLOPPY, DIGITAL				DRAWN BY: John... CHKD: III L	DATE: 10/18/78 3/20/80	ENGR: John... APPR: John...	DATE: 3/21/80	SIZE: B	8050002	REV G	SHT 4/5

REVSIONS			
REV	DATE	DESCRIPTION	APPROVED
SEE SHT #1			

DRAWN BY:	M. AMES	DATE:	5/1/80
CHGD BY:	L. LINDNER	VER:	5/1/80
ENGR:	J. HARRIS	APP'D BY:	J. HARRIS
REVIEWED:		TESTED:	
APPROVED:		NET WT:	
ITEM NO. 8050002		USED ON:	NET ASSY
		8050	80500010
		SIZE:	D 8050002
		SCALE:	INCHES
		NOTE:	SHEET 3 OF 5

-01 SHOWN



PART NO.	DESCRIPTION
8050006-01	PCB ASSY - FLOPPY ANALOG

REVISIONS		DATE	APPROVED
LTR	ZONE	DESCRIPTION	
A		PRODUCTION RELEASE	9/1/80 R.C.Little
B		REV PER ECO 1614	4-15-80 R.C.Little
C		REV PER ECO 1642	5-12-80 K.Kiffen
D		REV PER ECO 1834	8-28-80 K.Kiffen

IDENTIFY WITH COMMODORE
PART NO. 8050006 &
APPLICABLE DASH NO. LOCATE
AS SHOWN.

1. SHEET 5 OF 5 SIZE D
ASSY DWG

NOTES - UNLESS OTHERWISE SPECIFIED:

commodore	TITLE: PCB ASSY-FLOPPY ANALOG	DRAWN BY: J.Wilson	DATE 3/1/80	ENGR: <i>John</i>	APPR: <i>Nh>IN</i>	SIZE B	DRAWING NUMBER 8050006
		CMKD: <i>John Wilson</i>	3/1/80			SHEET 1 OF 5	

PART / DASH NO.	ITEM	QTY	PART NUMBER	DESCRIPTION	REF DES	BEN	NOTES
	REF 1	1	8050005	SCHEMATIC-FLOPPY ANALOG			
	2						
	3	1	8050007	PCB - FLOPPY ANALOG			
	4						
	16	5	900461-28	CAP, CER, Z5U .1 μ F, 50V, -80-20	C12,14,15,18,20,23,27,29,32,35-40,C5		
	16	6	900462-29	COG 47 μ F \pm 5 %	C42		
	17	1	900463-10	X7R 330 μ F \pm 10 %	C8		
	18	1	900463-12	X7R 470 μ F	C9,11		
	19	1	900463-18	X7R 1500 μ F	C2		
	20	1	900463-32	X7R .022 μ F	C6,7		
	11	1	900463-36	X7R .047 μ F \pm 10%	C13		
	12	1	900462-69	COG 2200 μ F 50V \pm 5 %	C1		
	13	1	900464-36	CER Z5U .47 μ F 50V \pm 20 %	C3,4,41		
	14	1	900020-05	MONOLITHIC 1 μ F 50V \pm 10 %	C22,31		
	15	1	900100-18	ELECTROLYTIC, 220 μ F 16V	C24,25		
	16	1	900462-41	▼ CER COG 150 μ F 50V, 10 %	C10		
	17	1	900402-11	CAP, DIPPED TANT, 3.3 μ F, 25V, 5 %	C16,17,19,21,26,28,30		
	18						
	19						
	20	1	900750-02	DIODE IN4002	CR22,23,26,27	ITEM 25 MAY BE USED AS A SUBSTITUTE	
	21	1	900750-03	IN4003	CR28-35		
	22	1	900850-17	IN4446	CRI-3,5-20,24,36,37		
	23	1	900948-03	IN5223 B	CR4		
	24	1	900948-15	DIODE IN5235 B	CR21,25		
	25	1	900150-05	DIODE IN4005			
	26						
	27	1	901301-05	CHOKE RF SHIELDED 10 μ h	L5,6		
	28	1	901301-01	CHOKE RF SHIELDED 100 μ h	L1,2,7		
	29	1	901301-06	CHOKE RF SHIELDED 22 μ h	L3,4		
	30						
	31						
	32	1	902658-03	TRANSISTOR 2N4400	Q3,4,5		
	33	1	902742-01	TRANSISTOR TIPIIO	Q1,2		
	34						
	35						
	36						
	37						

commodore

TITLE:

ASSY FLOPPY ANALOG

DRWN BY:
ML K12567
CHKD: M

DATE

ENGR:
APPR:

DATE

SIZE:
B

REV:
D

SHT:
2/5

QUANTITY	ITEM NO.	REF/DASH NO.	PART NUMBER	DESCRIPTION	REF DES	BEND	NOTES
01	2 38		901600-36	RES, FIXED, 1 Ω , 1/2 W, 5%	R47,69		
	1 39		901550-49	100 Ω , 1/4 W, 5%	R4		
	2 40		901550-89	150 Ω	R2,3		
	1 41		901550-100	180 Ω	R85		
	4 42		901550-52	220 Ω	R39,40,55,73		
	2 43		901550-76	270 Ω	R49,59		
	44						
	3 45		901550-14	330 Ω , 1/4 W	R14,15,10		
	1 46		901600-37	390 Ω , 1/2 W	R84		
	6 47		901550-58	470 Ω , 1/4 W	R5,6,44,46,66,68		
	1 48		901550-38	510 Ω	R9		
	1 49		901550-30	560 Ω	R18		
	8 50	B	901550-88	750 Ω	R75-82		
	17 51		901550-01	1K	R12,13,16,19,21,34,38,43,45,53,54,57,65,67,70,71,74		
	3 52		901550-69	1.5K	R17,20,22		
	3 53		901550-18	2.2K	R7,8,23		
	2 54		901751-21	3.16K	R25,27		
	1 55		901751-22	6.19K	R36		
	4 57		901550-20	10K	R41,51,60,62		
	7 58		901550-11	12K	R28-31,37,52,63		
	1 59		901550-55	18K	R11		
	1 60		901751-23	19.6K	R35		
	3 61		901550-12	22K	R1,24,26		
	2 62		901550-15	27K	R32,33		
	4 63		901550-22	47K	R42,56,64,72		
	2 64		901550-16	RES, FIXED, 150K, 1/4 W, 5%	R48,58		
	1 65		901550-04	RES, FIXED, 6.8K, 1/4 W, 5%	R83		
	2 66		902262-03	POTENTIOMETER, 5K	R50,61		
	67						
	68						
	2 69		901523-08	IC 592	U2,3		
	1 70		901523-04	311	U1		
	2 71		901523-11	741	U12,17		
	1 72		901510-01	9602	U7		
	1 73		901523-01	IC NE555	U8		
	74						
	75						

commodore

TITLE: ASSY FLOPPY ANALOG

DRAWN BY:
HAL KELSEY
CHKD BY: HLL

DATE: ENGR: APPR:

DATE: SIZE: B

SIZE: B 8050006 D

RFV SHT
3/5

commodore

TITLE

ASSY FLOPPY ANALOG

DRWN BY
M L KEE
CHECKED

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ENGR
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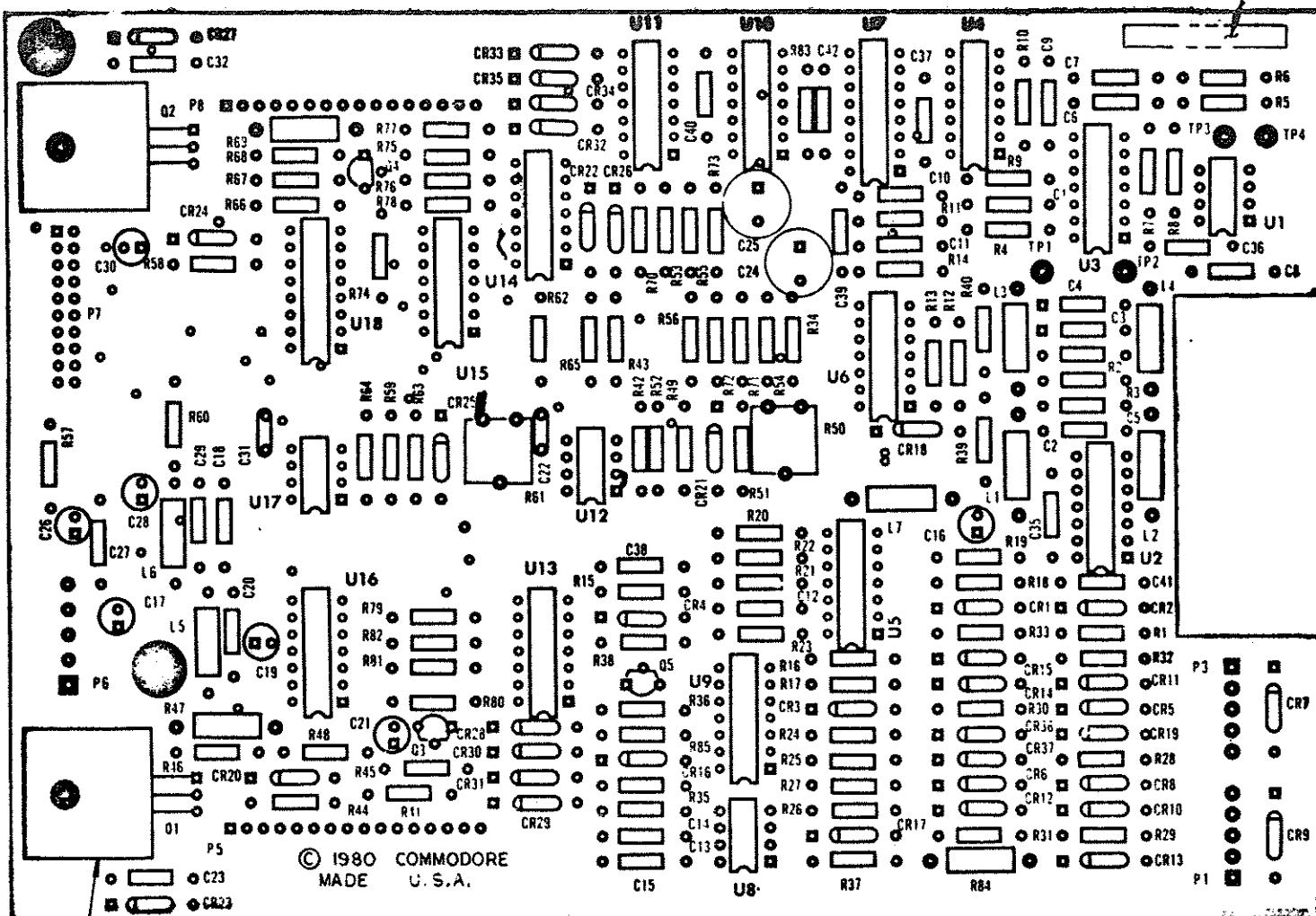
DATE _____

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REVISIONS			
LTR	ZONE	DESCRIPTION	DATE APPROVED
SEE SHT #1			



2 PL
93
92
91
90
89

UNLESS OTHERWISE SPECIFIED TOLERANCES ON DECIMALS		DRAWN BY V. AVES 3/1/80	DATE 3/1/80
CHK'D / / / / / ENGR / / / / / APPR / / / / / INSPECTED / / / / / MATERIAL FLOPPY		COMMODORE	P.C.B. ASSY, FLOPPY ANALOG
FINISH: FLOPPY BC50		USED ON: 80500001	SIZE D 80500001 REV D
		SCALE 2/1 SHEET 5 OF 5	